

Low-noise read-out electronics design for a solar soft X-ray spectrometer onboard the MSS-1B satellite

Authors: Jian-Wu Chen, Yong-Qiang Shi, Fuchang Zuo, Zhiwu Mei, Weichun Fu, Fangqin Gai, Ye Chang, Ying Yang, Shujuan Yang, Zhijun Tu, Xuli Liu, Jianfu Zhang, Ran Zheng, Li Wang, Chunhui Zhao, Zhang Di, Xiaoping Zhang, Jian-Wu Chen

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Abstract

The main scientific payload of Macau Science Satellite-1B is a solar soft X-ray detection unit. To obtain an accurate solar X-ray spectrum, we have designed low-noise, high-throughput electronics. Solar radiation is detected using a low-leakage silicon drift detector (SDD), which is cooled to -30°C . The SDD output is processed using two parallel shaping amplifiers with peaking times of 315 ns and 65 ns. The amplifiers are designed using two-pole multiple-feedback active low-pass filters optimized to achieve a Bessel response. The differential output of the shaping amplifier generates a bipolar signal. The phase of the differential stage is tuned to ensure zero crossing corresponding to the peak of the shaping amplifier. A high-speed switch is inserted between the shaping amplifier and the peak-hold capacitor, and the peak value is maintained by turning off the switch. Fast and slow peak-hold circuits share a common ADC via time-division multiplexing. Both peak values are sampled for space-background rejection. Traditional pile-up detection methods cannot distinguish pulses that overlap in a fast channel. In this study, the differential of the “fast shaping” is selected, enabling the distinction of events separated by as little as 65 ns, which is crucial for solar flare detection. The energy resolution is measured to be 138 eV at 5.90 keV. The centroid drift is less than 3.6 eV between -5°C and 20°C . Compared with other solar X-ray instruments, this study demonstrates improved energy resolution with a lower peaking time, indicating a higher solar flare detection capability.

Full Text

Preamble

Low-Noise Read-Out Electronics Design for a Solar Soft X-Ray Spectrometer Onboard the MSS-1B Satellite

Jian-Wu Chen^{1,†}, Yong-Qiang Shi¹, Fu-Chang Zuo¹, Zhi-Wu Mei¹, Wei-Chun Fu¹, Fang-Qin Gai¹, Ye Chang¹, Ying Yang¹, Shu-Juan Yang¹, Zhi-Jun Tu¹, Xu-Li Liu¹, Jian-Fu Zhang¹, Ran Zheng¹, Li Wang¹, Chun-Hui Zhao¹, Di Zhang², and Xiao-Ping Zhang³

¹Beijing Institute of Control Engineering, Beijing 100190, China

²School of Astronautics, Northwestern Polytechnical University, Xi'an 710072, China

³State Key Laboratory of Lunar and Planetary Sciences, Macau University of Science and Technology, Macau 999078, China

The main scientific payload of Macau Science Satellite-1B is a solar soft X-ray detection unit. To obtain accurate solar X-ray spectra, we have designed low-noise, high-throughput electronics. Solar radiation is detected using a low-leakage silicon drift detector (SDD) cooled to $-30\text{ }^{\circ}\text{C}$. The SDD output is processed by two parallel shaping amplifiers with peaking times of 315 ns and 65 ns. These amplifiers employ two-pole multiple-feedback active low-pass filters optimized to achieve a Bessel response.

The differential output of the shaping amplifier generates a bipolar signal. The phase of the differential stage is tuned to ensure zero crossing corresponds to the peak of the shaping amplifier. A high-speed switch is inserted between the shaping amplifier and the peak-hold capacitor, and the peak value is maintained by turning off the switch. Fast and slow peak-hold circuits share a common ADC via time-division multiplexing. Both peak values are sampled for space-background rejection. Traditional pile-up detection methods cannot distinguish pulses that overlap in the fast channel. In this study, the differential of the “fast shaping” is selected, enabling distinction of events separated by as little as 65 ns, which is crucial for solar flare detection. The energy resolution is measured to be 138 eV at 5.90 keV. The centroid drift is less than 3.6 eV between $-5\text{ }^{\circ}\text{C}$ and $20\text{ }^{\circ}\text{C}$. Compared with other solar X-ray instruments, this study demonstrates improved energy resolution with a lower peaking time, indicating higher solar flare detection capability.

Keywords: Solar flare, X-ray detector, Energy resolution, Pile-up rejection, Spectroscopy

Introduction

X-ray astronomy is based on the detection of X-rays from celestial sources [?]. The Macau Science Satellite-1B (MSS-1B) is primarily responsible for monitoring solar activity, particularly solar flares, during Solar Cycle 25 [?]. Its main

scientific payload is a solar X-ray detector (SXD) featuring a wide energy range and high count rate capability. The SXD consists of two soft X-ray detection units (SXDUs) with different aperture areas and two identical hard X-ray detection units (HXDUs). These four detection units are integrated into a single instrument. This study focuses on the electronic design of the SXDU.

The X-ray Sensor (XRS) on the GOES satellite provides continuous solar X-ray flux measurements, and the standard X-ray classification of solar flares is based on these measurements [?, ?]. However, this sensor lacks spectral resolution, which limits physical investigation of coronal emissions in the X-ray spectrum. The Solar Assembly for X-rays (SAX) on MESSENGER employs a Si-PIN detector with an aperture area of 0.03 mm^2 . Its energy resolution is 598 eV at 5.9 keV, constrained by Si-PIN noise [?]. The Solar X-ray Spectrometer (SXR) on the FY-2F mission monitors solar flares above the M1 level with an energy resolution of 185 eV [?]. The HEPP-X instrument onboard CSES is designed to monitor solar radiation from a wide field of view [?]. The X-ray spectrometer on MinXSS detects solar X-rays in the 0.8–12 keV range with a time cadence of 10 s [?]. The on-orbit energy resolutions for MinXSS-1 and MinXSS-2 at 5.90 keV are 240 eV for a 4.8 μs peaking time and 168 eV for 1.2 μs , respectively [?]. An improved instrument known as the Dual-Zone Aperture X-ray Solar Spectrometer employs a newer version of the SDD and a dedicated aperture design [?]. Its cadence time is set to 9 s, but solar observations are limited to flare levels below M3 due to detector saturation [?]. These CubeSats and instruments are designed using commercial off-the-shelf electronic devices, and one instrument was previously damaged by a single-event latch-up [?]. The Chandrayaan-2 Solar X-ray Monitor measures the 1–15 keV spectrum from lunar orbit with an energy resolution of 175 eV at 5.9 keV [?]. Its aperture area is limited to 0.367 mm^2 , making it feasible for detecting flares up to the M5 class.

We enhanced the time cadence to 1 s using a dedicated X-ray signal processing design. The peaking times of the shaping amplifiers were optimized to improve energy resolution while achieving higher count rates. In particular, two soft X-ray detector units with different aperture areas were integrated to detect solar irradiation, targeting both the quiet Sun and solar flares up to the X-class level. The primary working principle of the SXD was presented by Shi et al. [?], whereas this study focuses on a detailed description of the SXDU's electronic design and ground calibration results.

The remainder of this paper is organized as follows. Section II presents the basic principles, instrument composition, and specifications. Sections III–IV describe the design of the X-ray detector module and the analog electronic unit. Section V discusses the scientific data types, while Section VI covers spectral performance calibration. Finally, Section VII presents the conclusions.

Instrument Design

To distinguish emission lines from solar flares, the SXDU is designed with an energy resolution better than 200 eV across an energy range of 1–20 keV. The detector and electronics are identical for both SXDUs. Table 1 lists the specifications.

Table 1. Performance requirements of SXDU.

Parameter	Specification
Energy range (keV)	1–20
Energy resolution (eV)	< 200 @ 5.9 keV
Aperture area (cm ²)	0.17 and 0.005
Field of view (°)	$\geq 1 \times 10^5$
Time cadence (s)	1
Count rate (counts s ^{−1})	100,000

Figure 1 [Figure 1: see original paper] presents a conceptual sketch of the SXDUs. The units consist of X-ray Detector Modules (XDMs), analog electronic units (AEUs), a data processing unit (DPU), and a power supply unit (PSU). Collimators positioned in front of the XDMs restrict the field of view and aperture area. The two XDMs and their associated AEUs share a common DPU and PSU. Detector power is supplied by low- and high-voltage power supplies within the PSU, which are interconnected with the AEU and DPU via a common bus board for signal transmission and power distribution. Additionally, two temperature sensors are mounted on the AEUs to monitor temperature fluctuations on the analog board.

A. SDD Detector

Silicon X-ray detectors are widely used in space applications, including silicon drift detectors (SDDs) [?], silicon photomultipliers [?, ?], silicon microstrips [?, ?], CCD detectors [?, ?], and Si-PIN detectors [?]. A 500 μm thick SDD is selected as the X-ray sensor. Its unique electrode structure provides ultralow capacitance, enabling operation at short peaking times that increase the count rate. Energy resolution is further improved using a thermoelectric cooler (TEC). The SDD, along with a CMOS charge-sensitive preamplifier, is mounted on the TEC. The detector is positioned behind a thin window composed of a 150 nm silicon nitride film covered with 250 nm aluminum. The two XDMs use the same type of SDD, but the aperture areas restricted by the collimator are 0.17 cm² and 0.005 cm², respectively. A passive graded shield surrounding the detector package protects against space background radiation and fluorescent photons from surrounding materials. Additionally, two ²⁴¹Am radioactive sources are mounted between the detector plane and collimator in both SXDUs for in-flight calibration and self-verification.

B. TEC Characteristics

Detector leakage current is sensitive to operating temperature, doubling for every 7 °C increase, as described by [?]:

$$I_0 = T^2 e^{-E_g/2kT}$$

where I_0 is the leakage current, T is the detector operating temperature, E_g is the silicon bandgap energy (temperature-dependent), and k is the Boltzmann constant. The TEC reduces electronic noise in the detector and preamplifier. Heat extracted by the TEC is dissipated through a mounting stud attached to a copper heat radiator. Figure 3 [Figure 3: see original paper] shows the TEC voltage and current at a room temperature of 25 °C. The SDD temperature decreases exponentially as TEC power increases, allowing the silicon detector to be cooled to −30 °C. Since the leakage current of the SDD increases after radiation exposure [?], the TEC temperature can be adjusted via command, and the TEC can be turned off if necessary. The voltage and current of the TEC driver are recorded every second.

C. Preamplifier Reset Rate

When detector leakage current accumulates in the preamplifier, the ramp output eventually saturates. To counter this, a reset pulse is applied to restore the detector anode to ground potential, as shown in Fig. 2 [Figure 2: see original paper]. The large transient caused by the reset pulse can interfere with signal processing; therefore, a window of 10 μs is implemented to eliminate its effects. The preamplifier periodically resets the charge accumulated on the feedback capacitance due to X-ray signals and leakage current:

$$I_0 = C_f V_{ramp} f_{reset}$$

where I_0 is the leakage current, C_f is the preamplifier feedback capacitance (typically 25 fF), V_{ramp} is the ramp voltage, and f_{reset} is the ramp-reset rate. The reset rate decreases exponentially with decreasing detector temperature and stabilizes at a nominal value of 12–13 Hz in the absence of incident radiation. However, it increases with higher incidence rates and deposited energy. To monitor variations in SDD leakage current following radiation exposure, the reset rate is recorded every second.

IV. AEU

The AEU comprises an SDD preamplifier driver, TEC power driver, and front-end readout electronics, as shown in Fig. 4 [Figure 4: see original paper].

A. SDD and TEC Drivers

The preamplifier driver circuit generates and filters power, sends reset signals to the preamplifier, and buffers and amplifies the preamplifier output to drive the front-end electronics. The TEC driver reads the temperature diodes inside the SDD package and supplies power to the TEC. The temperature diode is biased using a 160 μA current source. A proportional-integral-derivative (PID) control feedback loop maintains the SDD at the desired temperature, which is set via a digital-to-analog converter (DAC). TEC power is supplied by a high-efficiency DC-DC converter, with output ripple minimized using an LC-type filter. The voltage and current of the TEC driver, along with the set and measured temperatures, are converted using an ADC. These parameters, together with the ramp-reset rate, are used to assess the operational status of the SDD in orbit.

B. Shaping Amplifier

The preamplifier generates a ramp output composed of small steps. Its output is differentiated to measure the step voltage. The shaping amplifier, designed using two-pole multiple-feedback active low-pass filters [?] as shown in Fig. 5 [Figure 5: see original paper], enhances the signal-to-noise ratio. The amplifiers are optimized to achieve a Bessel phase response, ensuring constant group delay within the passband. The step response exhibits no overshoot or ringing, which is essential for spectrometer performance. While the rising edge of the shaping output is steep, the voltage decays exponentially back to the baseline. These exponential tails extend many times beyond the full width at half maximum (FWHM) duration. Pulses that overlap during this extended window cause pile-up, particularly at high count rates. Reducing the FWHM mitigates pulse pile-up but introduces additional noise.

Two parallel shaping amplifiers with different time constants are designed to serve distinct purposes. The first amplifier is optimized for better timing accuracy, while the second achieves higher spectral resolution. The shaping amplifiers with peaking times of 315 ns and 65 ns are referred to as “slow shaping” and “fast shaping,” respectively. The fast-shaping channel improves double-pulse resolution and reduces pile-up but introduces more noise, consequently requiring a higher threshold set by a DAC. In contrast, the slow-shaping channel uses a longer peaking time to extract spectral information. When the amplitude of the slow-shaping channel exceeds the threshold, the peak value is held by the peak holder and digitized by an ADC.

Table 2 lists the design parameters of the slow-shaping channel. The group delay is simulated to be constant in the passband at 853.8 ns, as shown in Fig. 6 [Figure 6: see original paper]. A pulse generator connected to the shaping amplifier input simulates X-ray events. Figure 7 [Figure 7: see original paper] shows the transient response of the slow channel, revealing no overshoot and no ringing. The rise time of the slow-shaping amplifier is measured to be 315 ns,

the fall time is 945 ns, and the pulse width is less than 2.8 μs .

Traditional pile-up detection uses a fast/slow-channel approach [?] but cannot distinguish pulses that overlap in the fast channel. The differential of the shaping amplifier generates a bipolar output. As Fig. 8 [Figure 8: see original paper] shows, the pulse width of the differential signal is significantly smaller than that of the shaping amplifier. Therefore, the differential signals of the fast channel are selected for pile-up rejection.

The time interval of an X-ray photon can be described by an exponential distribution:

$$f(\tau) = \lambda e^{-\lambda\tau}$$

where τ is the dead time and λ is the average incoming rate, assumed to be $1 \times 10^5 \text{ counts} \cdot \text{s}^{-1}$. The dead time caused by the shaping amplifier is 2.8 μs for the slow channel. The probability of non-paralyzable pile-up can be calculated as:

$$P = \frac{\lambda\tau}{1 + \lambda\tau}$$

For the slow channel with $\tau = 2.8 \mu\text{s}$, the probability is 21.9%. This is reduced to 4.8% for the fast channel with a dead time of 0.5 μs . The throughput is significantly better, making it suitable for calculating the true input rate.

C. Peak-Hold Circuit

The rise time of the SDD output is a function of the incident position [?]. The peaking time of the slow channel is significantly longer than the variation in rise time, leading to pulse height loss due to ballistic deficit. Consequently, events originating far from the center could be rejected by comparing fast and slow amplitudes. Both peak values are sampled for space-background rejection.

The peak-hold circuit diagram is shown in Fig. 9 [Figure 9: see original paper]. A CMOS analog switch is inserted between the shaping amplifier and the hold-capacitor stage. When a pulse peak is detected, the peak value is held in the capacitor by turning off the switch until it is read out by a 16-bit ADC (only the 12 most significant bits are stored). The charge on the capacitor is reset by turning on the switch. The peak value from the capacitor is buffered by a unit-gain operational amplifier with low input-bias current. Low-leakage, low-dielectric absorption capacitors are essential. The fast-shaping amplitude peak value is obtained similarly, with both fast and slow peak-hold circuits sharing a common ADC. Figure 10 [Figure 10: see original paper] shows the ADC input waveform.

A CMOS analog switch selects a single channel. The switch features extremely high switching speed with a maximum transition time of 30 ns. Therefore,

the slow-channel amplitude does not depend on event position. The flat-top duration of the fast channel is shorter than the rise time, ensuring break-before-make switching. Charge injection is less than 2 pC, which is crucial for high-precision peak-height capture.

Figure 11 [Figure 11: see original paper] shows the timing of the switches in the peak-hold circuit. The peak-hold signal durations “PH_F” and “PH_S” for the fast and slow channels are 150 ns and 200 ns, respectively. The ADC conversion signal “CNV” starts with delays of 100 ns and 150 ns for the two channels, respectively. At the end of each peak-hold signal, the switch changes state. Note that “PH_F” may be absent in some cases since the fast channel threshold is higher. Consequently, “PH_{MUX}” assertion is controlled by both “HOLD_S” and “PH_F”. Here, “HOLD_S” is the “HOLD” signal of the slow channel, while “HOLD_F” corresponds to that of the fast channel. At the rising edge of “HOLD_S”, if “PH_F” is high, then “PH_{MUX}” is asserted at the falling edge of “PH_F”; otherwise, “PH_{MUX}” is asserted immediately.

The “HOLD” signal is generated by connecting the differentiating amplifier output to a comparator with hysteresis. “HOLD_S” and “HOLD_F” are generated using the same approach, as shown in Fig. 12 [Figure 12: see original paper] and Fig. 13 [Figure 13: see original paper]. The comparator U2 output, known as “HOLD”, switches high for input voltages greater than V_{HI} and does not switch low until the input voltage falls below V_{LO} (i.e., at zero crossing).

$$V_{HI} = (V_{CC} - 1) \frac{R1}{R1 + R2}$$

$$V_{LOW} = 0$$

where $V_{CC} = 5$ V, $R1 = 100$ Ω , $R2 = 10$ k Ω , and $V_{HI} = 40$ mV. The latch pin of U2 is connected to the output of U1. Therefore, “HOLD” remains in a low state unless the shaping-amplifier amplitude exceeds the threshold.

D. Pile-Up Rejection

If two pulses are too close in time, the height of the second pulse can be affected. Errors in measured pulse height may distort the spectrum. Overlapping pulses in the slow channel are identified using the differential signal of the fast channel, as described earlier. To obtain a clean spectrum, all overlapping pulses are rejected. The analog shaper exhibits an infinite impulse response. Even when the tail drops rapidly toward the baseline, a small contribution remains [?]. The dead-time window widths “WINDOW_S” and “WINDOW_F” for the slow and fast channels are 2.8 μ s and 0.5 μ s, respectively, as shown in Fig. 14 [Figure 14: see original paper]. If an event occurs during dead time, the dead time is reset, reducing throughput. Therefore, dead time can be described using a paralyzable model, as shown in Fig. 15 [Figure 15: see original paper] [?].

V. Operating Mode Design

The SXDU generates three types of observation modes in addition to the default standby mode. In standby mode, after power-on, the SXDU operates with the XDMs and AEUs powered off. A selected detector channel is powered by command. Following initialization, the SXDU switches to the selected observation mode.

The baseline noise observation mode enables detailed monitoring of detector performance, particularly for evaluating noise after radiation exposure. The signal amplitude is sampled every 10 ms, regardless of incident events.

In spectrometer histogram observation mode, each valid pulse height is stored in its respective channel to obtain a spectrum histogram with 2048 channels. The histogram depth is 3 bytes, generating 6 KB of histogram data every second.

The photonlist observation mode samples pulse heights in both channels sequentially using a 16-bit ADC. Data for each event consists of the detector number, trigger mode, time stamp, and pulse height data. Each transfer frame consists of 512 events, and data capacity is limited by the interface clock to a maximum of 40 frames per second.

VI. Spectral Performance

A linear energy response is desirable in solar spectroscopy. Before applying X-ray sources, a step signal generator is connected to the front-end readout electronics instead of an X-ray detector. The step voltage is attenuated 80 times using a small resistor to reject noise. The sampled peak values scale linearly with the input voltage, as shown in Fig. 16 [Figure 16: see original paper].

A miniature X-ray tube with a W target is used to obtain fluorescence lines from aluminum (1.49 keV), titanium (4.51 keV), manganese (5.90 keV), copper (8.05 keV), molybdenum (17.48 keV), and silver (22.17 keV). The emission line at approximately 13.94 keV from the ^{241}Am radioactive source is also included. Spectral performance is evaluated using the K α line centroid and width. During calibration, the detector temperature is maintained at -30°C .

Figure 17 [Figure 17: see original paper] shows linear calibration using X-ray sources. Gain and offset are obtained by linear fitting. The gain is 5.9 eV/channel when operating in photonlist observation mode. The offset is approximately -233 eV with an error of 42 eV. The upper and lower limits of the energy range are 23.39 keV and 0.7 keV, respectively.

The fraction without loss is:

$$P(X > \lambda) = e^{-\lambda\tau}$$

Figure 18 [Figure 18: see original paper] shows the X-ray detector response to Mn fluorescence illumination. The 5.90 keV and 6.49 keV line complexes are

clearly detected. Spectral lines are fitted with a Gaussian function to obtain the peak channel centroid and FWHM. The measured energy resolution is 138 eV at 5.90 keV, markedly better than the 200 eV requirement. Compared to instruments listed in Table 4, this study demonstrates superior energy resolution with a lower peaking time, indicating higher sensitivity to solar flare levels and better spectral resolution.

As shown in Fig. 19 [Figure 19: see original paper], the centroid drift in the temperature range of -5°C to 20°C is less than 0.6 ADC bin, which calibrates to approximately 3.6 eV. These results demonstrate the low noise and high accuracy of the electronic readout design for solar X-ray spectrometers.

VII. Conclusion

This study presents the design and ground calibration of a solar soft X-ray detection unit on the MSS-1B satellite. The core component of the X-ray module is a silicon drift detector, chosen for its excellent energy resolution. The detector is nominally cooled to -30°C using a TEC. The preamplifier output voltage is processed by two parallel shaping amplifiers with peaking times of 315 ns and 65 ns. The slow-shaping channel, optimized for spectroscopy, operates with a longer peaking time, while the fast channel is used for pile-up rejection. Linearity response was validated using an electronic signal generator and various X-ray sources. The measured energy resolution is 138 eV at 5.90 keV, with a centroid drift of less than 3.5 eV over a temperature range of -5°C to 20°C . Compared with other on-orbit solar X-ray instruments, this study demonstrates improved energy resolution with a lower peaking time, enabling higher sensitivity to solar flare levels and better spectral resolution. These results highlight the low-noise, high-accuracy readout electronics design of the solar X-ray spectrometer.

Table 3. Energy resolution calibrated by various X-ray sources.

X-ray Source	Energy (keV)	FWHM (eV)	Error (eV)
Al-K α	1.49	152	3
Ti-K α	4.51	140	2
Mn-K α	5.90	138	2
Cu-K α	8.05	142	3
^{241}Am	13.94	145	4
Mo-K α 1 17.48 148 5 Ag - K α 1	22.17	155	6

Table 4. Energy resolutions of solar X-ray detectors.

Instrument	Sensor	Area	Energy Range	Field of View	Energy Resolution	Peaking Time	Count Rate	Cadence
SXRS [?]	Si-PIN	7 mm ²	1.5–24.8 keV	$\pm 26^\circ 185^\circ \times 185^\circ$ — 2.05 s HEPP — X [?]	50 eV @ 5.90 keV keV HEPP — SDD 7 mm ²	$\pm 30^\circ \times \pm 15^\circ$ 10 kcps SXM [?] SDD 30 mm ²	175 cts/s keV MinXSS [?] SDD 25 mm ²	1.0 s 80 kcps
This study	SDD	25 mm ²	0.7–23.97 keV	—	138 eV @ 5.90 keV	315 ns	100 kcps	1 s

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